

Protocol Demo Board Manual 1

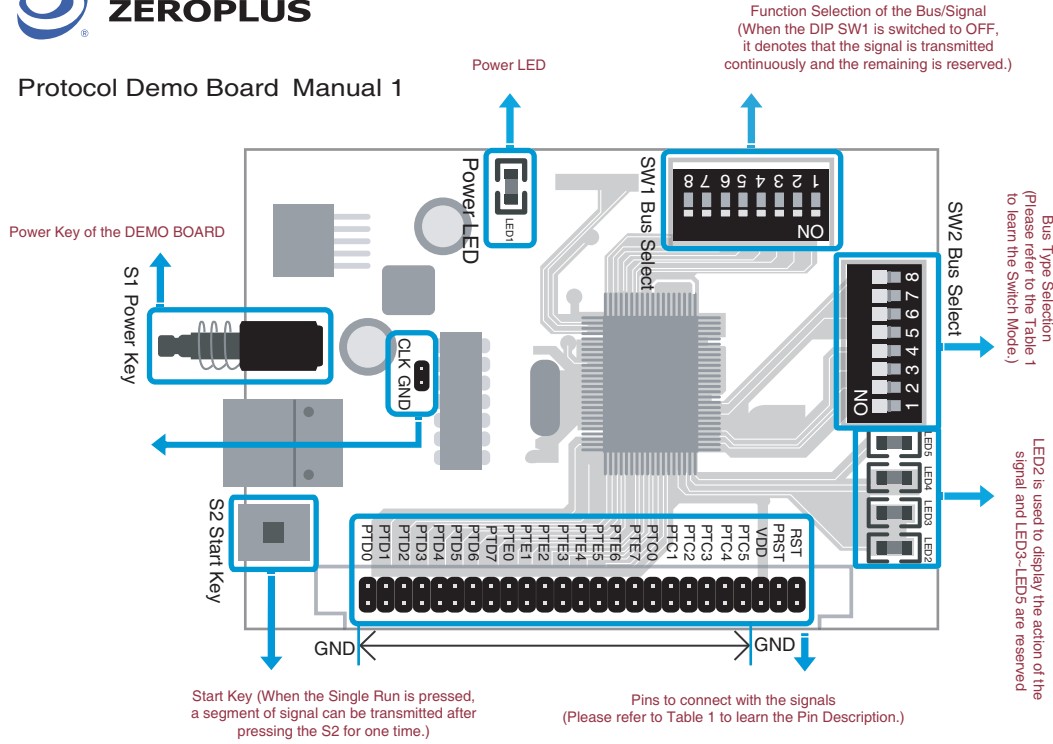


























Table1: Bus Switch List

NO.	Bus Name	Switch Mode of SW2 (Dip Switch)	Pin Description	NO.	Bus Name	Switch Mode of SW2 (Dip Switch)	Pin Description	NO.	Bus Name	Switch Mode of SW2 (Dip Switch)	Pin Description	NO.	Bus Name	Switch Mode of SW2 (Dip Switch)	Pin Description
18	MOD Bus		MODBUS=PTD0	24	LPC-SERIRQ		LCLK =PTD0 SERIRO =PTD1	30	MCU-51 DECODE		P0.0-P0.7=PTD0-PTD7 P2.0-P2.7=PTE0-PTE7 ALE=PTC0 PSEN=PTC1	37	AC97		SYNC=PTD0 BITCLK=PTD1 SDI/SDO=PTD2 RESET=PTD3
19	7-SEGMENT LED		A=PTD0 B=PTD1 C=PTD2 D=PTD3 E=PTD4 F=PTD5 G=PTD6 COM=PTD7 DP=PTE0	25	JTAG2.0		TCK=PTD0 TMS=PTD1 TDI/O=PTD2 TREST=PTD3	31	NEC _PD6122		carrier wave mode DATA=PTD0 no carrier wave mode DATA=PTD1	38	SLE4442		CLK=PTD0 I/O=PTD1 RST=PTD2
20	ST Bus		FR=PTD0 SDA=PTD1 CLK=PTD2	26	DSA Interface		DATA=PTD0 STB=PTD1 ACK=PTD2	32	PM Bus1.1		CLK=PTD0 DATA=PTD1	39	CCIR656		DB0=PTD0 DB1=PTD1 DB2=PTD2 DB3=PTD3 DB4=PTD4 DB5=PTD5 DB6=PTD6 DB7=PTD7 CLK=PTE0 Decimal[1]=PTE1 Decimal[0]=PTE2
21	IRDA		IRDA_SIR=PTD0 ASK_IR=PTD1 IRDA_HDLC=PTD2 IRDA_FIR=PTD3	27	ST7669		3-Wire Mode : /CS=PTD0 SCL=PTD1 SI=PTD2 4-Wire Mode : /CS=PTD4 A0=PTD5 SCL=PTD6 SI=PTD7	33	PSB Interface		SD0=PTD0	40	PECI		PECI=PTD0
22	DMX512		DMX_D+=PTD0 DMX_D-=PTD1	28	LCD12864		RS/CS=PTD0 WR/SID=PTD1 E/SCLK=PTD2 DB[0]=PTD3 DB[1]=PTD4 DB[2]=PTD5 DB[3]=PTD6 DB[4]=PTD7 DB[5]=PTE0 DB[6]=PTE1 DB[7]= PTE2	34	SM Bus 2.0		SMBLCK=PTD0 SMBDAT=PTD1	41	3-WIRE		CS=PTD0 SCLK =PTD1 I/O =PTD2
23	FLEXRAY 2.1A		TXD=PTD0 TXEN =PTD1	29	PCM		SCLK=PTD0 FS=PTD1 DT=PTD2 DR=PTD3	35	DIGRF		CTRL mode CTRLLEN=PTD0 CTRLCLK=PTD1 CTRLDATA=PTD2 RXTX mode RXTX_EN=PTD3 SYCLK=PTD4 RXTX_DATA=PTD5	42	J-K FLIP-FLOP		CP=PTD0 CLR=PTD1 PR=PTD2 J/J=PTD3 K/K=PTD4 Q=PTD5 Q=PTD6